| Ref<br># | Hits | Search Query                                   | DBs   | Default<br>Operator | Plurals | Time Stamp       |
|----------|------|--|---|---------------------|---------|------------------|
| L1       | 3856 | "nonvolatile" near4 (write or written)         | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/08/18 18:27 |
| L2       | 786  | "bit line" and 1                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/08/18 18:27 |
| L3       | 669  | transistor and 2                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/08/18 18:27 |
| L4       | 515  | substrate and 3                                | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/08/18 18:42 |
| L5       | 265  | @ad<="20020110" and 4                          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/08/18 18:38 |
| L6       | 6    | "addressing voltage" and "write voltage" and 1 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/08/18 18:38 |
| L7       | 3    | @ad<="20020110" and 6                          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/08/18 18:38 |